


PCB SENSE LINE CONNECTIONS:

- * = SEPERATE VOUT LINE - TIE R4, C1, AND SP1 TO TP5.
- ** = SEPERATE GND LINE - TIE TP4, R11, R3, R14 AND C1 TO U1 PIN 1.

DRAWN BY:	TIM KLEMAN	DATE:	03/23/2011	ENGINEER:	THEJU BERNARD	DATE:	
RELEASED BY:		DATE:		TITLE:	ISL75051SRH EVALUATION BOARD SCHEMATIC		
UPDATED BY:		DATE:		TESTER:		MASK#	HRDWR ID
						ISL75051SRHEVAL1Z	REV. C
		FILENAME:				SHEET	1 OF 1

~/ISL75051SRH/ISL75051SRHEVAL1ZC